



Design and Analysis of Integrator-Based Log-Domain Filter (THE KLUWER INTERNATIONAL SERIES IN ENGINEERING AND)

Gordon W. Roberts, Vincent W. Leung

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This title deals with the design and analysis of log-domain filter circuits. It describes synthesis methods for developing bipolar or BiCMOS filter circuits with cut-off frequencies ranging from the low kilohertz range to several hundred megahertz. Numerous examples provide measured experimental data from IC prototypes.

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